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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/759,756	01/16/2004	Steven M. Waldstein	06486.P001	8344
75	590 03/21/2005	EXAMINER		
James C. Sche	•	TAN, VIBOL		
BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP Seventh Floor			ART UNIT	PAPER NUMBER
12400 Wilshire		2819		
Los Angeles, CA 90025-1026			DATE MAILED: 03/21/2005	

Please find below and/or attached an Office communication concerning this application or proceeding.

	b					
	Application No.	Applicant(s)				
	10/759,756	WALDSTEIN ET AL.				
Office Action Summary	Examiner	Art Unit				
	Vibol Tan	2819				
The MAILING DATE of this communication ap Period for Reply	pears on the cover sheet w	ith the correspondence address				
A SHORTENED STATUTORY PERIOD FOR REPL THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1. after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a rep - If NO period for reply is specified above, the maximum statutory period - Failure to reply within the set or extended period for reply will, by statut Any reply received by the Office later than three months after the mailir earned patent term adjustment. See 37 CFR 1.704(b).	136(a). In no event, however, may a oly within the statutory minimum of thi will apply and will expire SIX (6) MO te, cause the application to become A	reply be timely filed rly (30) days will be considered timely. NTHS from the mailing date of this communication. BANDONED (35 U.S.C. § 133).				
Status						
 1) ⊠ Responsive to communication(s) filed on 16 J 2a) ☐ This action is FINAL. 2b) ☐ This 3) ☐ Since this application is in condition for allowated closed in accordance with the practice under the condition of the condition o	s action is non-final. ance except for formal mat					
Disposition of Claims						
5) Claim(s) is/are allowed. 6) Claim(s) is/are rejected. 7) Claim(s) is/are objected to.	4a) Of the above claim(s) is/are withdrawn from consideration. Claim(s) is/are allowed. Claim(s) is/are rejected. Claim(s) is/are objected to.					
Application Papers						
9) The specification is objected to by the Examina 10) The drawing(s) filed on is/are: a) accomposed and applicant may not request that any objection to the Replacement drawing sheet(s) including the correct to by the E	cepted or b) objected to e drawing(s) be held in abeya ction is required if the drawing	nce. See 37 CFR 1.85(a). g(s) is objected to. See 37 CFR 1.121(d).				
Priority under 35 U.S.C. § 119						
12) ☐ Acknowledgment is made of a claim for foreign a) ☐ All b) ☐ Some * c) ☐ None of: 1. ☐ Certified copies of the priority document 2. ☐ Certified copies of the priority document 3. ☐ Copies of the certified copies of the priority document application from the International Bureat* See the attached detailed Office action for a list	nts have been received. Its have been received in a point documents have been au (PCT Rule 17.2(a)).	Application No n received in this National Stage				
Attachment(s)						
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date	Paper No	Summary (PTO-413) (s)/Mail Date Informal Patent Application (PTO-152)				

Application/Control Number: 10/759,756

Art Unit: 2819

DETAILED ACTION

Election/Restrictions

- 1. Restriction to one of the following inventions is required under 35 U.S.C. 121:
 - Claims 1-10, drawn to electronic digital logic circuitry, classified in class
 326, subclass 32.
 - II. Claims 11-18, drawn to multiplex communications, classified in class 370, subclass 241+.

The inventions are distinct, each from the other because of the following reasons:

- 2. Inventions I and II are unrelated. Inventions are unrelated if it can be shown that they are not disclosed as capable of use together and they have different modes of operation, different functions, or different effects (MPEP § 806.04, MPEP § 808.01). In the instant case the CMOS circuit comprising a first gate reference voltage, a first bias current is not intended to be used together with the physical layer segment including a transmitter and a receiver. The group I recites a CMOS circuit whereas the group II recites a physical layer segment for multiplexing circuit. Therefore, they have different modes of operation.
- 3. Because these inventions are distinct for the reasons given above and have acquired a separate status in the art as shown by their different classification, restriction for examination purposes as indicated is proper.

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4. Because these inventions are distinct for the reasons given above and the search required for Group I is not required for Group II, restriction for examination purposes as indicated is proper.

Applicant is advised that the reply to this requirement to be complete must include an election of the invention to be examined even though the requirement be traversed (37 CFR 1.143).

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Vibol Tan whose telephone number is (571) 272-1811. The examiner can normally be reached on Monday-Friday (7:00 AM-4:30 PM).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mike J. Tokar can be reached on (571) 272-1812. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

